

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS:

Young-woo PARK et al.

CONF. NO.: 6330

**SERIAL NO.:** 

09/008,525

GROUP:

2811

FILED:

January 16, 1998

EXAMINER: Nguyen, Cuong Quang

FOR:

SEMICONDUCTOR MEMORY DEVICE AND METHOD OF

MANUFACTURING FOR PREVENTING BIT LINE OXIDATION

## **RESPONSE TO ELECTION/RESTRICTION REQUIREMENT**

Assistant Commissioner for Patents Washington, D.C. 20231

February 13, 2003

Dear Sir:

Responsive to the Examiner's Election/Restriction Requirement dated January 13, 2003, the following remarks are respectfully submitted in connection with the above-referenced application.

## REMARKS

The Examiner has required an election of one of the following embodiments:

embodiment 1 described in Fig. 4 to Fig. 7;

embodiment 2 described in Fig. 8;

embodiment 3 described in Fig. 9 to Fig. 10;

embodiment 4 described in Fig. 11; and

embodiment 5 described in Fig. 12.

FEB 19 2003

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